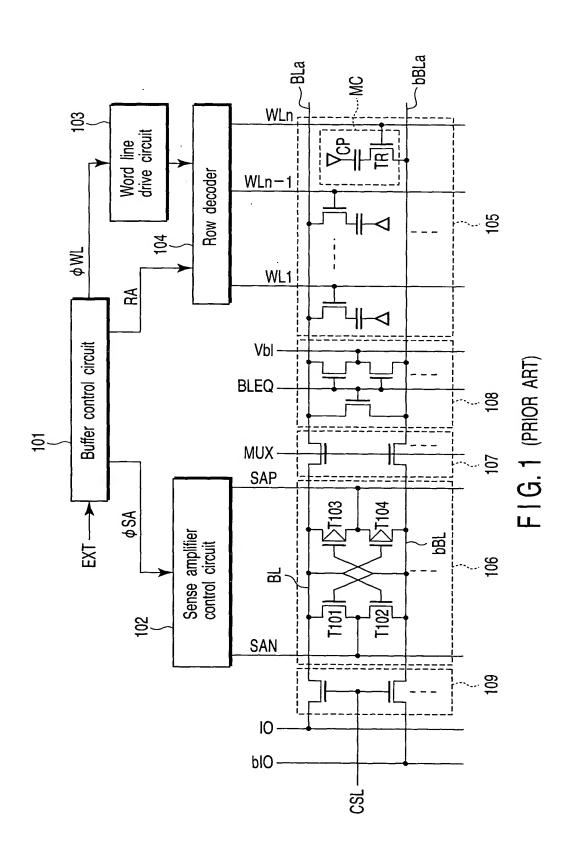
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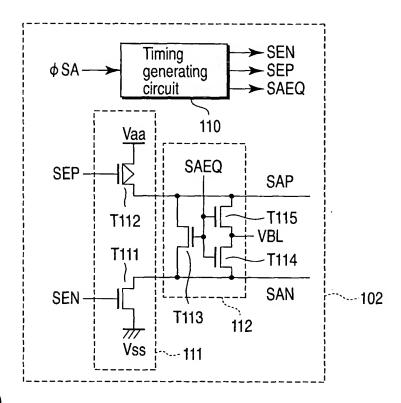
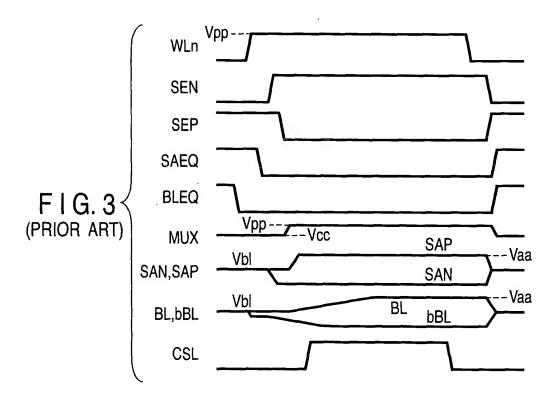
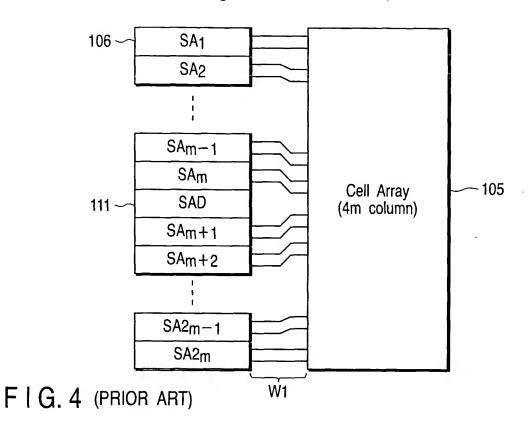


FIG. 2 (PRIOR ART)



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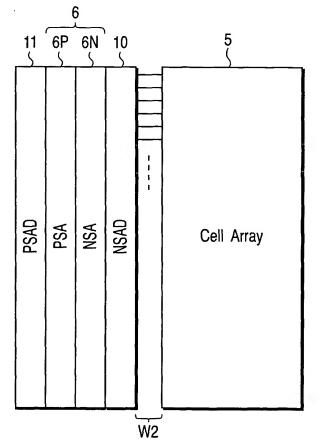
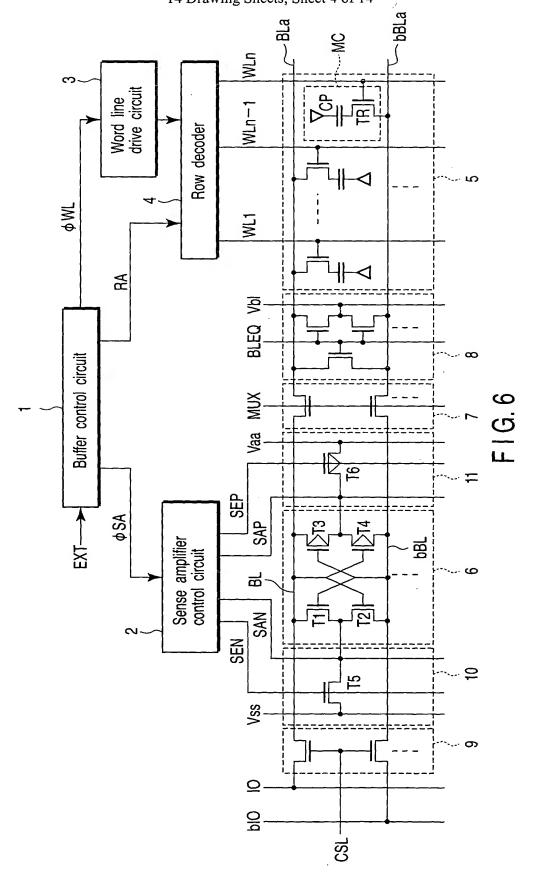
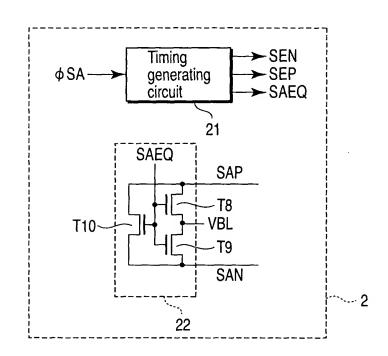


FIG. 5

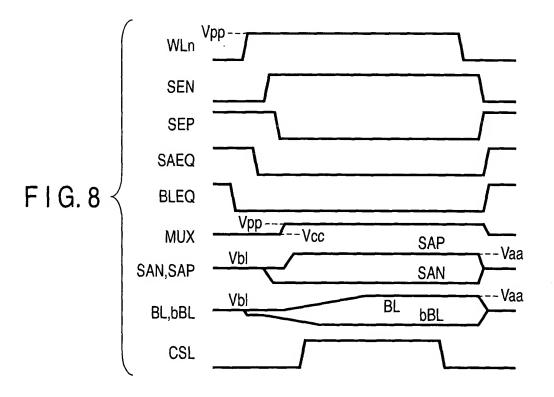
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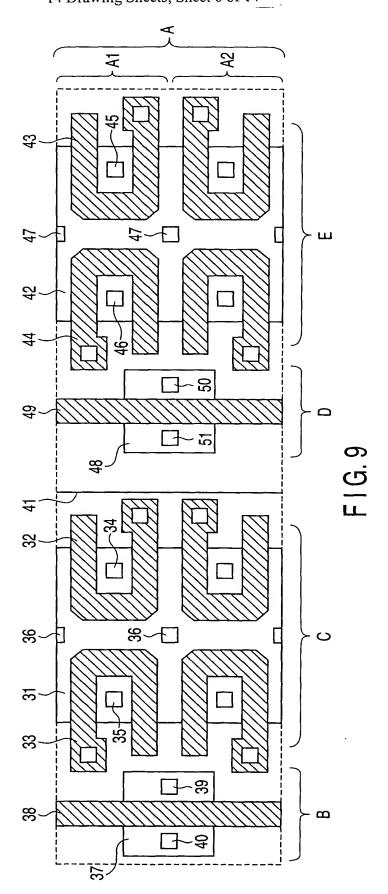
Hogan & Hartson 81790.0299 Takahiko HARA et al. Semiconductor Integrated Circuit... EV 325 217 491 US 14 Drawing Sheets; Sheet 5 of 14



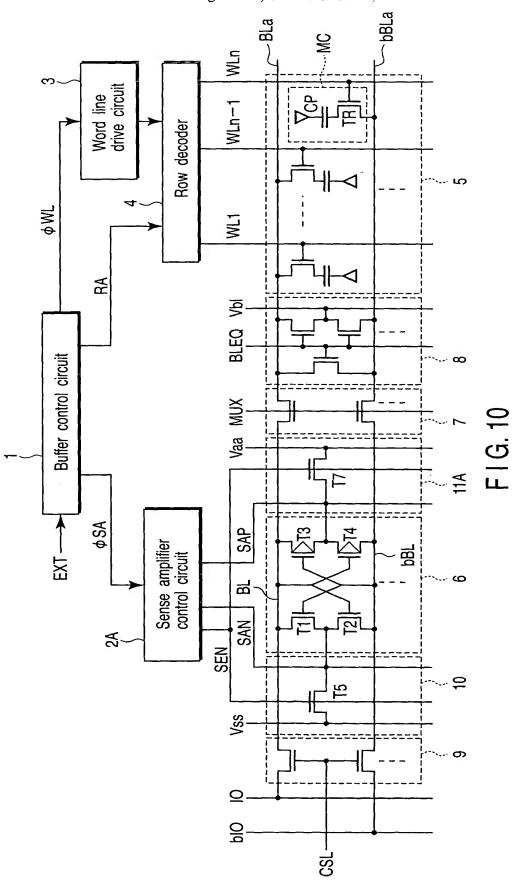




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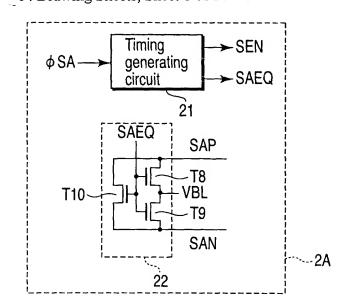
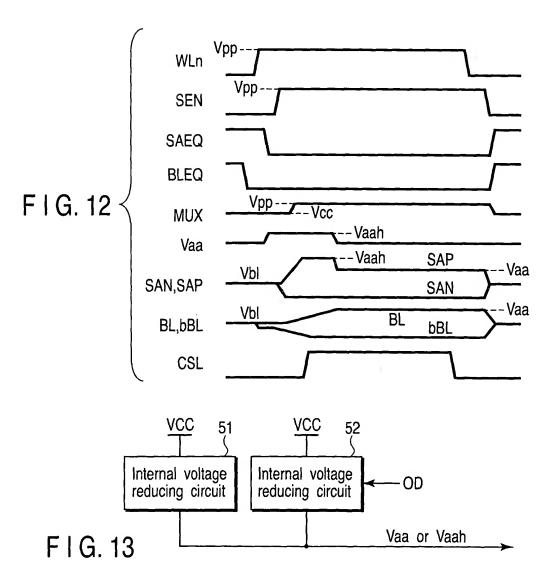
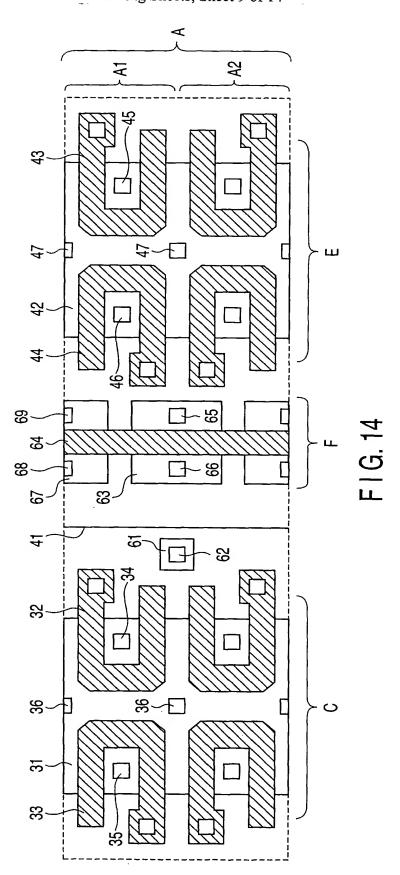


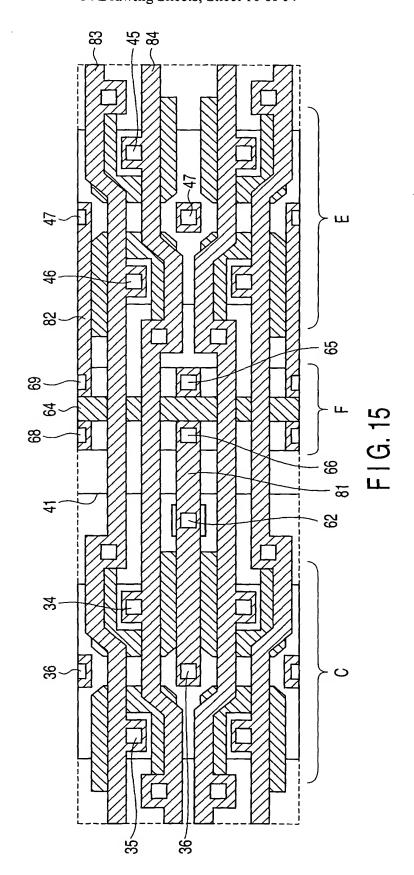
FIG. 11



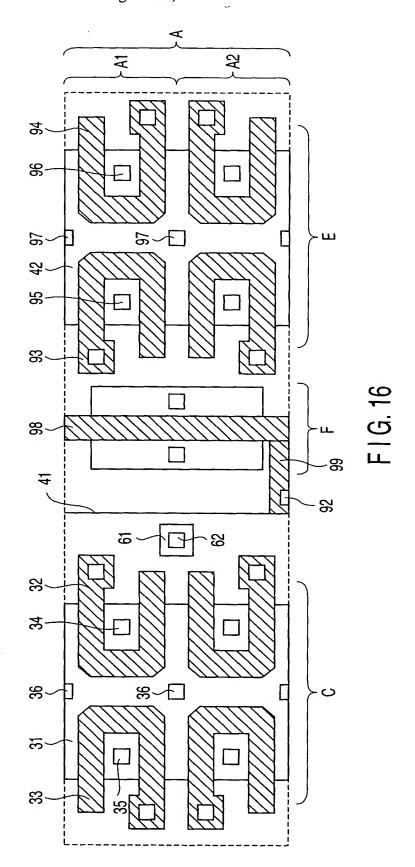
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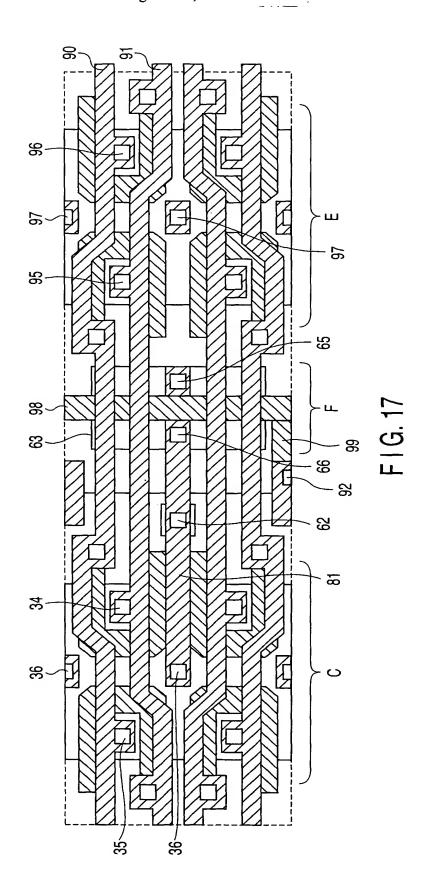
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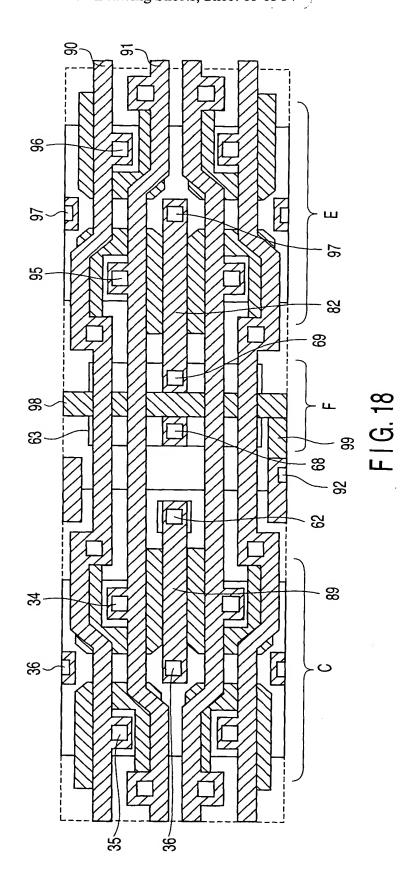
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